

### (12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

# (19) World Intellectual Property Organization International Bureau



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# (43) International Publication Date 24 December 2003 (24.12.2003)

### **PCT**

# (10) International Publication Number WO 03/107432 A1

(51) International Patent Classification<sup>7</sup>: H01L 29/78, 29/08, 29/10

(21) International Application Number: PCT/EP03/06117

(22) International Filing Date: 10 June 2003 (10.06.2003)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

02291458.4

12 June 2002 (12.06.2002) EP

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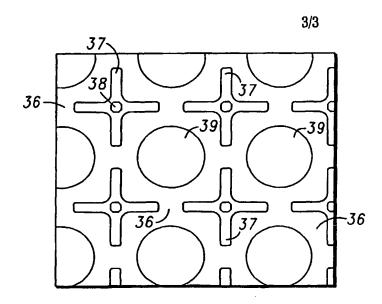
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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

### Published:

with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: POWER SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME



(57) Abstract: A low on-state resistance power semiconductor device has a shape and an arrangement that increase the channel density and the breakdown voltage. The power semiconductor device comprises a plurality of individual cells formed on a semiconductor substrate (62). individual cell comprises a plurality of radially extending branches (80) having source regions (37) within base regions The plurality of individual cells are arranged such that at least one branch of each cell extends towards at least one branch of an adjacent cell and wherein the base region (36) of the extending branches merge together to form a single and substantially uniformly doped base region (36) surrounding drain islands (39) at the surface of the semiconductor substrate (62).





CLASSIFICATION OF SUBJECT MATTER PC 7 H01L29/78 H01L29/08 H01L29/10 According to International Patent Classification (IPC) or to both national classification and IPC B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC 7 H01L Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) **EPO-Internal** C. DOCUMENTS CONSIDERED TO BE RELEVANT Relevant to claim No. Citation of document, with Indication, where appropriate, of the relevant passages 1-14 PATENT ABSTRACTS OF JAPAN X vol. 013, no. 571 (E-862), 18 December 1989 (1989-12-18) -& JP 01 238173 A (SANYO ELECTRIC CO LTD), 22 September 1989 (1989-09-22) cited in the application abstract; figures 5,2,3 US 5 399 892 A (NEILSON J M S ET AL) 21 March 1995 (1995-03-21) 1-9 X cited in the application column 3, line 16 - line 34; figures 4-6 column 3, line 49 -column 4, line 19; 10-12,14A figure 10 -/--Patent family members are listed in annex. X Further documents are listed in the continuation of box C. Special categories of cited documents : "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the 'A' document defining the general state of the art which is not considered to be of particular relevance Invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "E" earlier document but published on or after the International document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such docu- O document referring to an oral disclosure, use, exhibition or ments, such combination being obvious to a person skilled other means \*P\* document published prior to the international filing date but later than the priority date claimed \*&\* document member of the same patent family Date of mailing of the international search report Date of the actual completion of the international search 04/09/2003 27 August 2003 Authorized officer Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo ni, Fax: (+31-70) 340-3016 Morvan, D

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